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Docket: 0756-1614

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9/25/98

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of )  
Takeshi FUKUNAGA et al. )  
Serial No. 08/781,920 ) Art Unit: 1762  
Filed: December 30, 1996 ) Examiner: M. Padgett  
For: METHOD OF FABRICATING A )  
SEMICONDUCTOR DEVICE )  
UTILIZING A CATALYST )  
. MATERIAL SOLUTION ) Date: September 23, 1998

**PRELIMINARY AMENDMENT**

Honorable Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

In response to the Office Action mailed May 26, 1998, please amend the subject application as follows.